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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 10/529,539
Applicant : Hiroyuki Yoshikawa et al.
Filed : March 29, 2005
Title : "BROADBAND MODULATION PLL, TIMING ERROR
CORRECTING SYSTEM FOR BROADBAND MODULATION PLL,
MODULATION TIMING ERROR CORRECTING METHOD AND
METHOD OF ADJUSTING RADIO COMMUNICATION DEVICE
HAVING BROADBAND MODULATION PLL"

Confirmation No. : TBD
TC/A.U. : TBD
Examiner : TBD

Customer No. : 052054
Docket No. : 38038

Mail Stop PCT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT "B"

Sir:

Please amend the above-identified application, prior to examination thereof, in the following manner.

Amendments to the Drawings begin on page 2 of this paper and include both an attached replacement sheet and an annotated sheet showing changes.

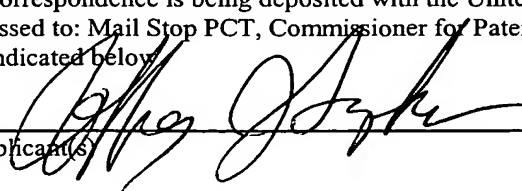
Remarks/Arguments begin on page 3 of this paper.

An **Appendix** including amended drawing figures is attached following page 3 of this paper.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Mail Stop PCT, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date indicated below.

Jeffrey J. Sopko

Name of Attorney for Applicant(s)



August 31, 2005

Signature of Attorney/Date